ABSTRACT OF THE DISCLOSURE

A control circuit receives the digital input data of n-bit and controls horizontal drivers so as to provide a liquid crystal panel with voltage corresponding to the input data during a 1H

5   cycle based on standard voltage. In addition, the control circuit creates gradation data by inverting each bit of the input data and controls the horizontal drivers so as to provide the liquid crystal panel with voltage in response to the output gradation data during the subsequent 1H cycle. In this case,

10 gradation-γ correction voltage relation is symmetrical with respect to a point in the center between the top gradation step and the bottom gradation step.